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In digital circuit design, register-transfer level (RTL) is a design abstraction which models a synchronous digital circuit in terms of the flow of digital signals (data) between hardware registers, and the logical operations performed on those signals.. Register-transfer-level abstraction is used in hardware description languages (HDLs) like Verilog and VHDL to create high-level ...

Register-transfer level - Wikipedia

RTL-SDR and GNU Radio with Realtek RTL2832U [Elonics E4000/Raphael Micro R820T] software defined radio receivers. Originally meant for television reception and streaming the discovery and exploitation of the separate raw mode used in FM reception was perhaps first noticed by Eric Fry in March of 2010 and then expanded upon by Antti Palosaari in Feb 2012 who found that these devices can output ...

rtl-sdr and GNU Radio w/Realtek RTL2832U, E4000 and R820T

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Embedded Design Handbook - intel.com

The Fundamentals of Efficient Synthesizable Finite State Machine Design using NC-Verilog and BuildGates Clifford E. Cummings Sunburst Design, Inc.

The Fundamentals of Efficient Synthesizable Finite State

In computer engineering, a hardware description language (HDL) is a specialized computer language used to describe the structure and behavior of electronic circuits, and most commonly, digital logic circuits.. A hardware description language enables a precise, formal description of an electronic circuit that allows for the automated analysis and simulation of an electronic circuit.

Hardware description language - Wikipedia

The RTL SDR V3 (or, Eight Bits On HF) By D. B. Gain Purpose and Scope; To see what you get using an \$20 8bit ADC (Analog to Digital Converter chip featuring an ENOB of 7)

The RTL SDR V3 - UDXF - Utility DXers Forum

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Catapult HLS - Mentor Graphics

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RTL-SDR Tutorial: Cheap ADS-B Aircraft RADAR

This information is for board designers who must determine FPGA pin usage, to create board layouts. The board design process sometimes occurs concurrently with the RTL design process.

External Memory Interface Handbook Volume 2: Design Guidelines

How to receive DVB-S digital video transmissions from the International Space Station with a bit of hardware and a lot of software.

SDR reception of Digital Amateur TV from the ISS - pabr.org

The R820T2 is the main tuner chip used in most RTL-SDR dongles. Several months ago Rafael Micro ceased regular production of their R820T2 chip, and the older R820T has also been discontinued for some time too.

R820T2 Chip Discontinued: Low Cost R820T2 RTL-SDRs will

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